IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Lee et al.

Serial No.: To be assigned Filed: Currently herewith

For: INTEGRATED CIRCUIT DEVICES FORMED THROUGH SELECTIVE

ETCHING OF AN INSULATION LAYER TO INCREASE THE SELF-ALIGNED CONTACT AREA ADJACENT A SEMICONDUCTOR REGION (as amended)

Date: September 16, 2003

Mail Stop PATENT APPLICATION Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT CITATION UNDER 37 C.F.R. § 1.97

Sir:

Attached is a list of documents on form PTO-1449. Items 1-103 listed on the PTO-1449 were cited in parent application Serial No. 09/702,597, filed October 1, 2000. Because the benefit of this application is claimed under 35 U.S.C. §120, no copies need to be furnished in accordance with 37 C.F.R. §1.98(d); however, copies will be furnished on request. It is requested that these documents be considered by the Examiner and officially made of record in accordance with the provisions of 37 C.F.R. §1.97 and Section 609 of the MPEP.

Respectfully submitted,

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Traci A. Brown

| Substitute form 1449A/PTO | | | | Complete if Known | | |
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|] | | | | Application Number | TBA | |
| INFORMATION DISCLOSURE STATEMENT BY APPLICANT | | | | Filing Date | Concurrently herewith | |
| | | | • | First Named Inventor | Jae-Goo Lee | |
| | | | | Group Art Unit | | |
| (use as many sheets as necessary) | | | | Examiner Name | | |
| Sheet | 1 | of | 1 | Attorney Docket Number | 5649-835DV | |

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^{*}EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.